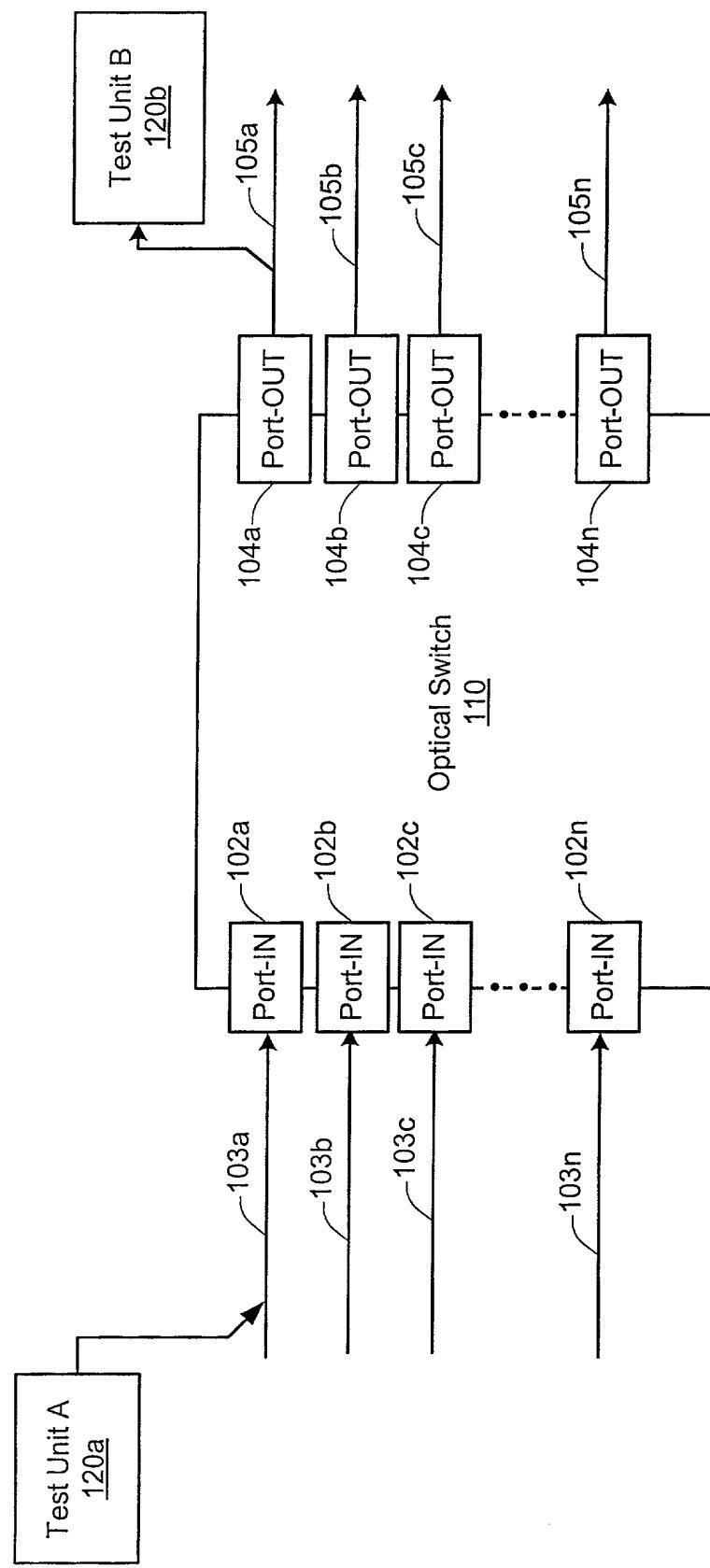


Fig. 1
(Prior Art)

Fig. 2
(Prior Art)



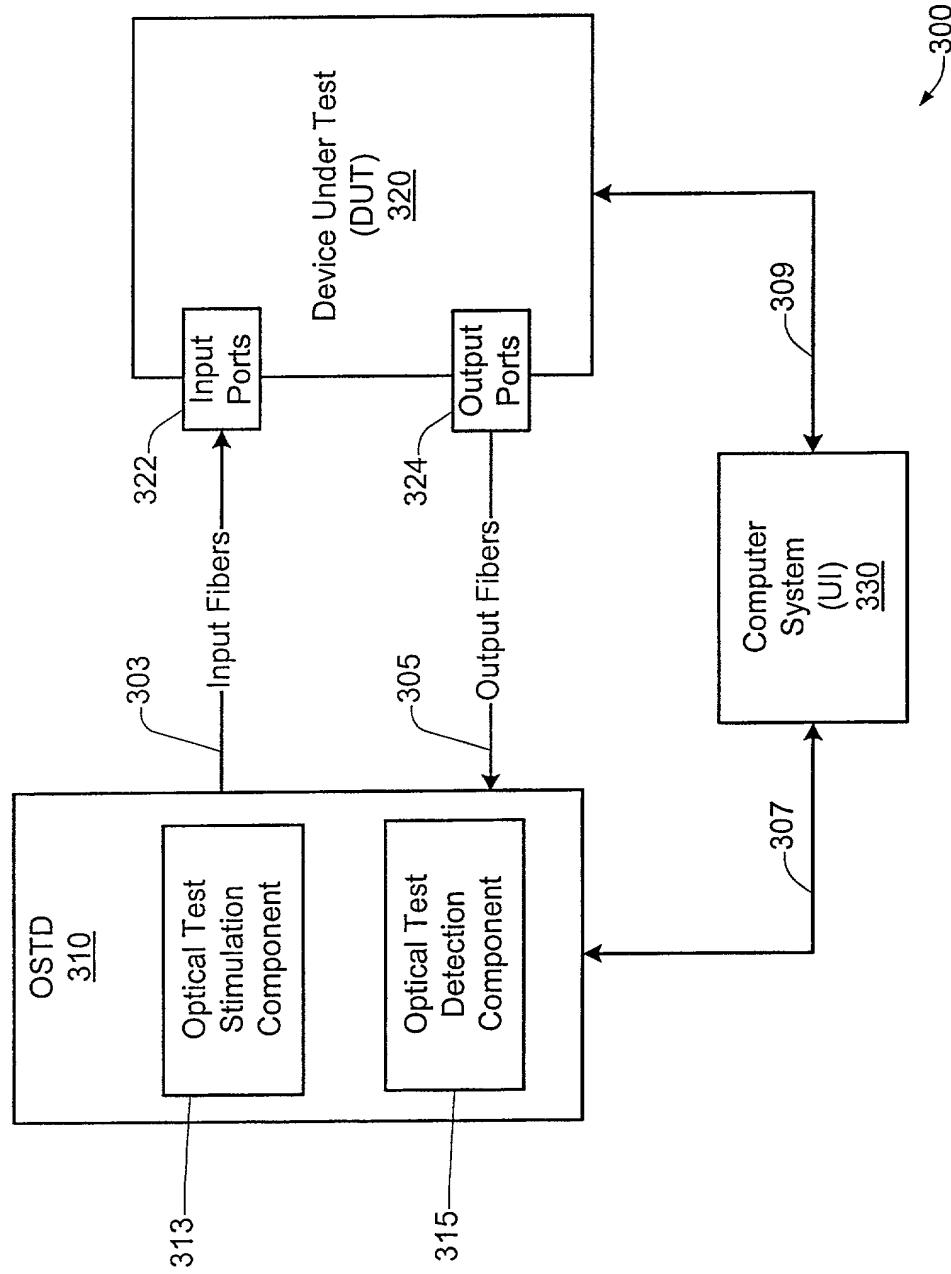


Fig. 3

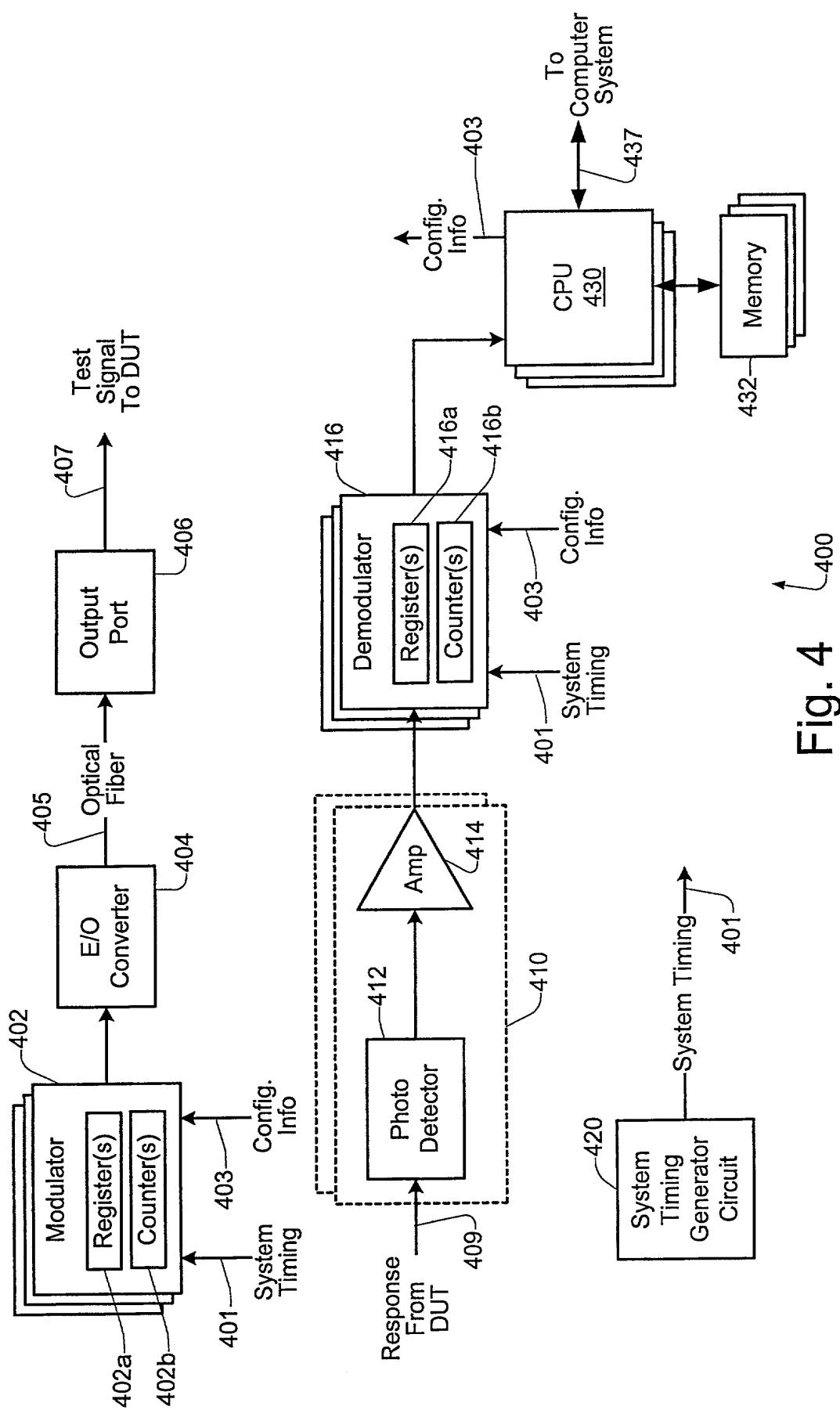


Fig. 4

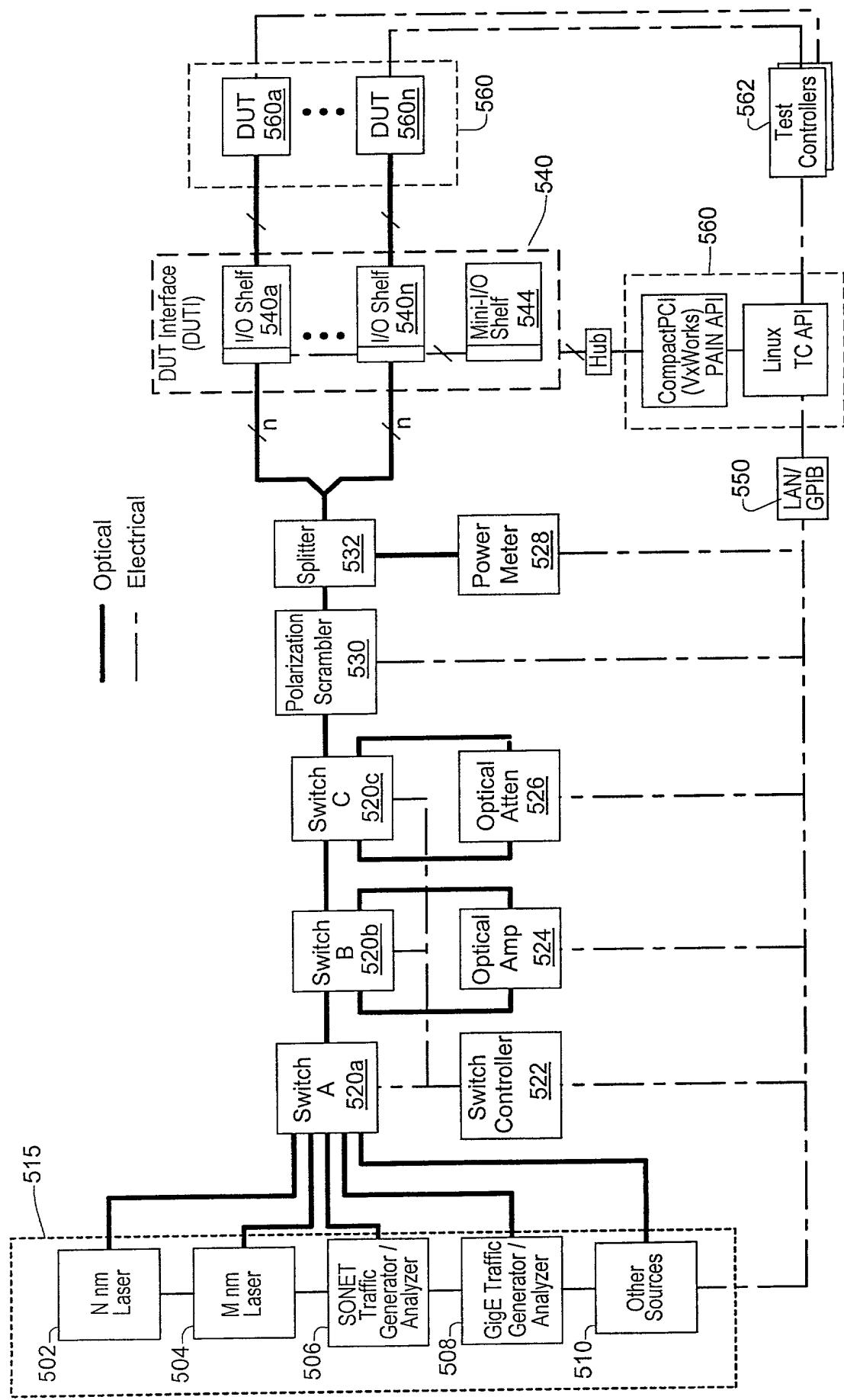


Fig. 5
500

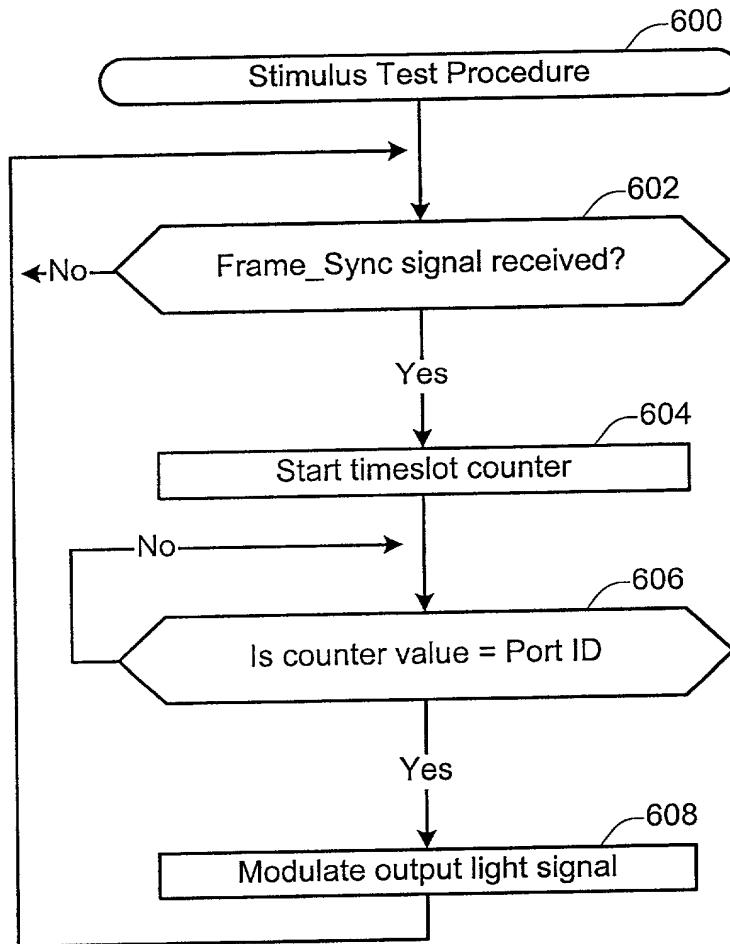


Fig. 6

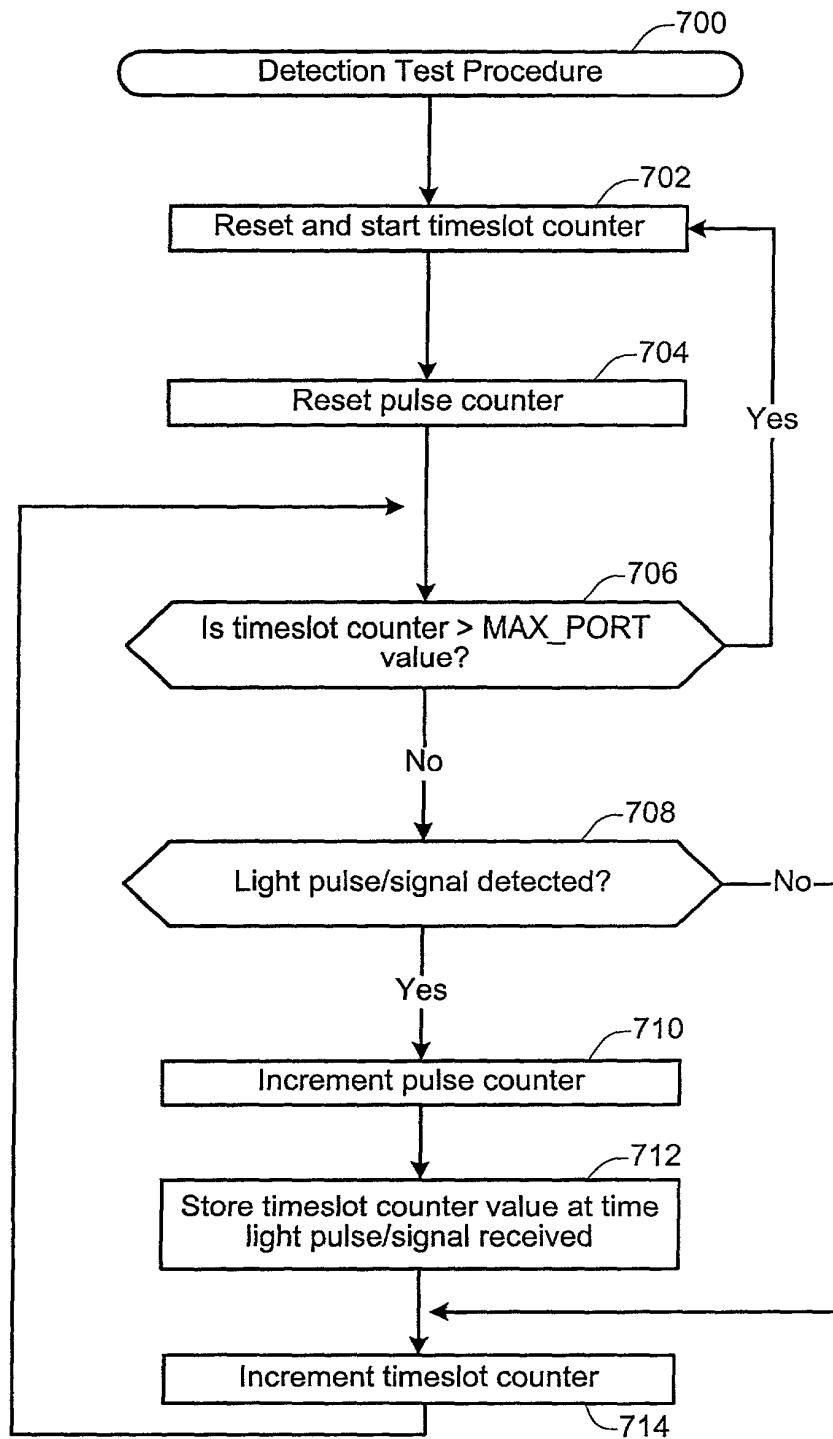


Fig. 7

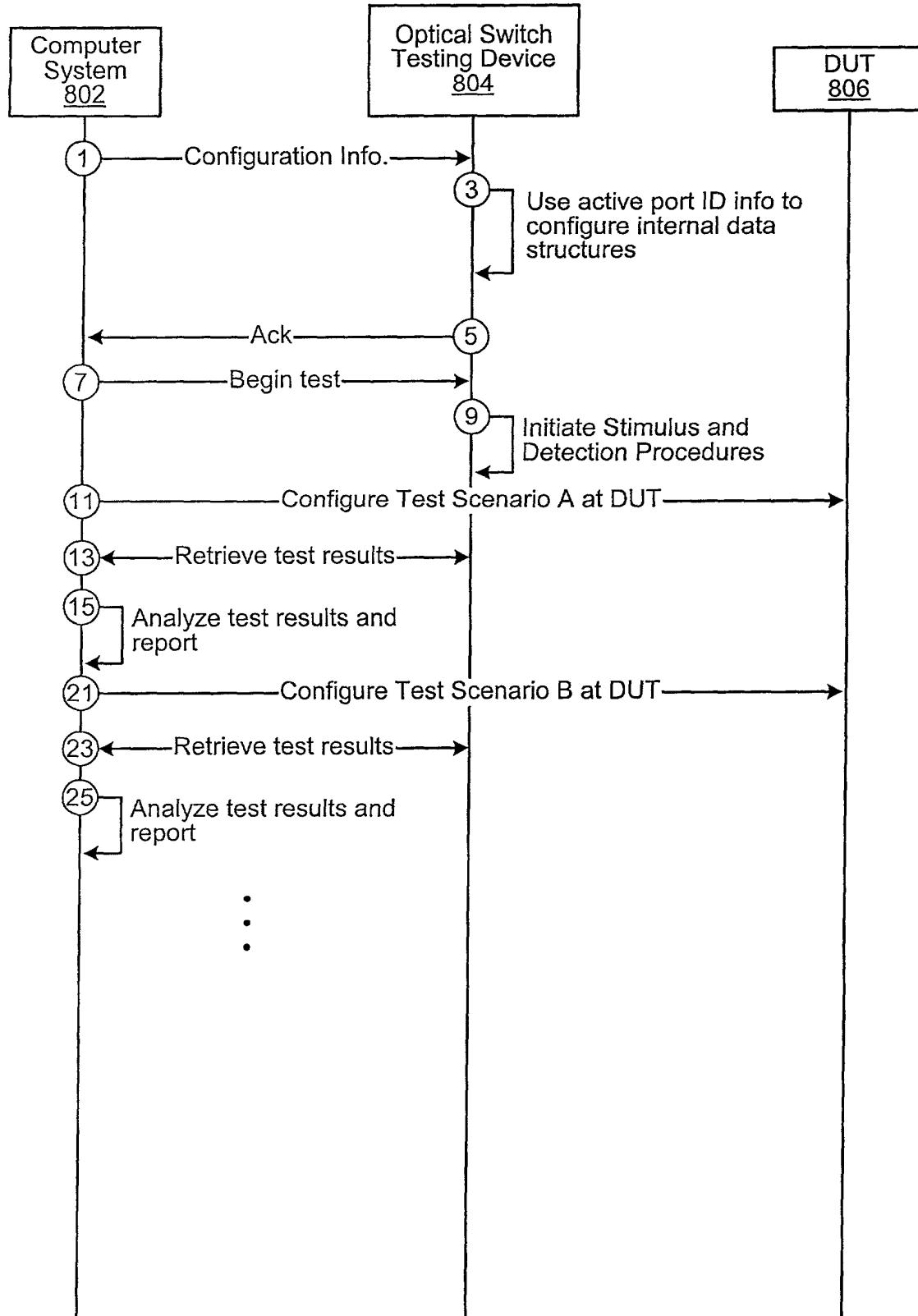


Fig. 8

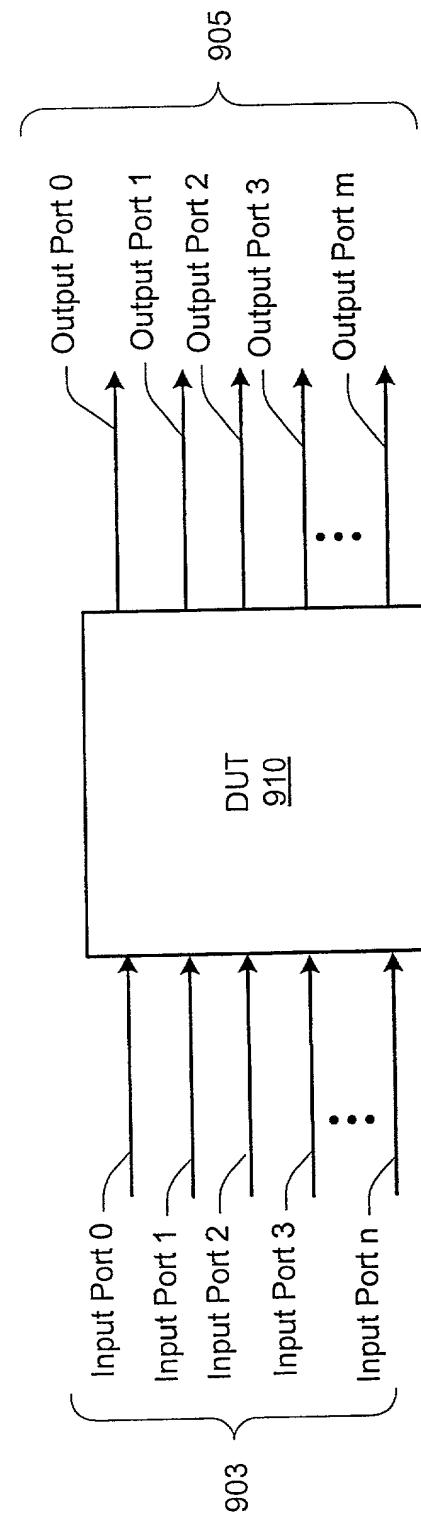


Fig. 9

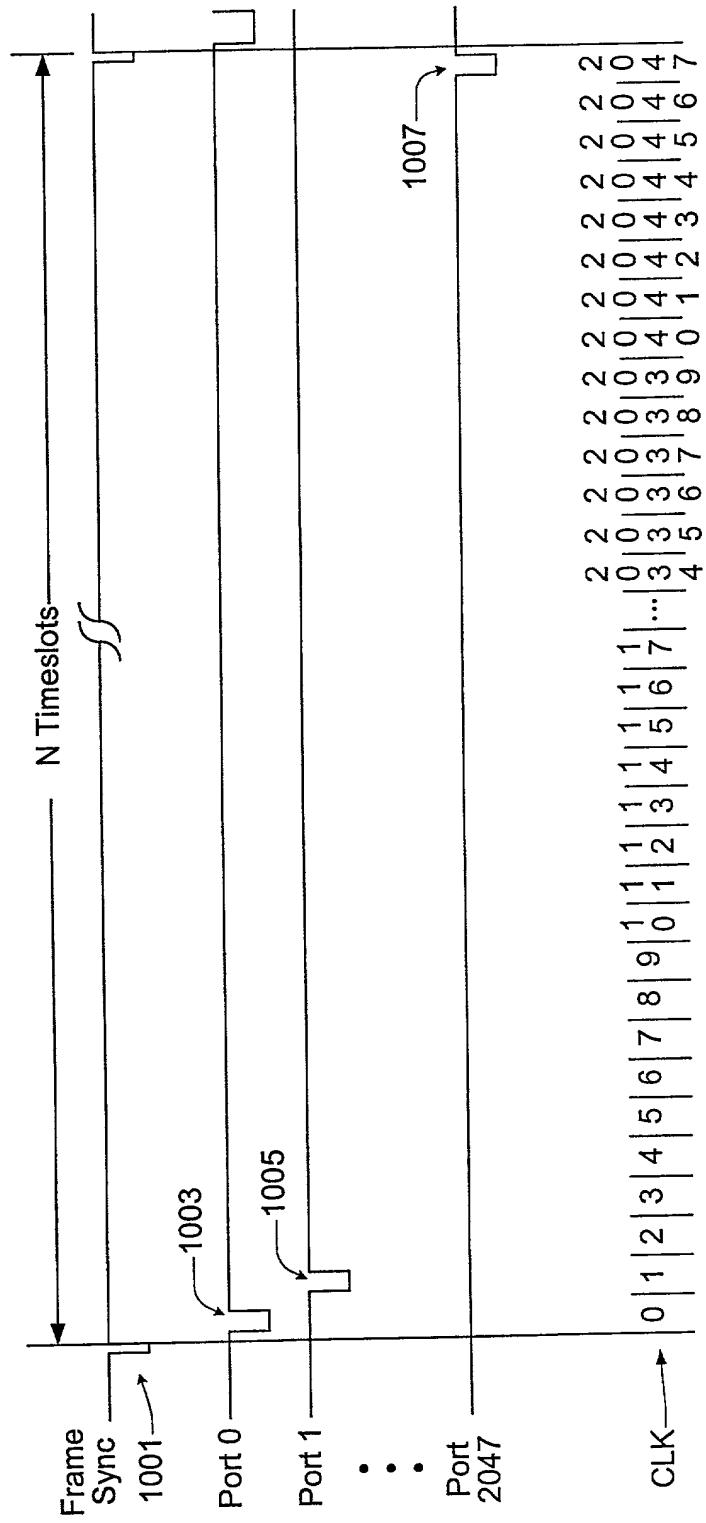


Fig. 10

		Output Ports <u>1104</u>			
		Output Port 0	Output Port 1	Output Port 2	Output Port 3
Input Ports <u>1102</u>	Input Port 0	Insertion Loss Value A	Insertion Loss Value B	...	
	Input Port 1	Insertion Loss Value C	Insertion Loss Value D	...	
	Input Port 2	Insertion Loss Value E	Insertion Loss Value F	...	
	Input Port 3	

Fig. 11A

		Output Ports <u>1104</u>			
		Output Port 0	Output Port 1	Output Port 2	Output Port 3
Input Ports <u>1102</u>	Input Port 0	Time Value A	Time Value B	...	
	Input Port 1	Time Value C	Time Value D	...	
	Input Port 2	Time Value E	Time Value F	...	
	Input Port 3	

Fig. 11B